

Application Number: 10/577,055

Date of faxing: 3/10/2008

REMARKS

As far as the applicant understands, Olmstead does not disclose a multistage DAC, but discloses synchronization between the switched capacitor filter and the DAC. The synchronization between the switched capacitor filter and the DAC is unrelated to the synchronization between the stages disclosed in the present application.

In addition, the amplifier in claim 47 is an open loop transconductance amplifier while Olmstead does not disclose an open loop transconductance.

As written by the examiner, James's system includes an integrator. As far as the applicant understands, James's integrator does not integrate a digital signal. Moreover, the multi-stage digital to analog waveform converter of claim 45 is summed with an analog correction, rather than a predefined constant and therefore cannot be related to James.

The applicant submits that amended claim 45 clearly indicates how the multi-stage digital signals construction is performed.

The applicant requests the examiner to amend claim 51 as indicated above. In the amended claim 51, the parallel setting of the plurality of digital to analog waveform converters improves the resolution and reduces mutual noise when several outputs are desired. The outputs of the plurality of digital to analog waveform converters are added up, and therefore the system functions as a single digital to analog converter. Amended claim 51 discloses two or more digital to analog waveform converters with different signals to convert. This is in contrast with Matsusaka that uses the DSP (mentioned in col. 12 lines 18-21) only for supplying samples to the DAC. As far as the applicant understands, no crosstalk treatment is mentioned and therefore Matsusaka is irrelevant to the embodiment of amended claim 51.

Reconsideration of claims 45-51 in view of the amendments and the remarks is respectfully requested.

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Respectfully submitted

/Dan Raphaeli/

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